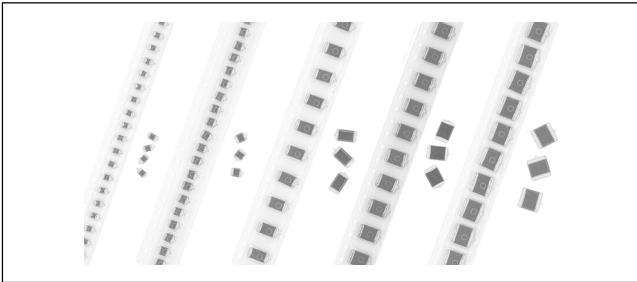




Solid Tantalum Chip Capacitors

TANTAMOUNT®, Low Profile, Conformal Coated, Maximum CV



FEATURES

- New case size offerings.
- 2mm height
- Terminations: Tin (2) standard.
- Low ESR
- 8mm, 12mm tape and reel packaging available per EIA-481-1 and reeling per IEC 286-3. 7" [178mm] standard. 13" [330mm] available.
- Footprint compatible with EIA 535BAAC and CECC 30801 molded chips.

PERFORMANCE CHARACTERISTICS

Operating Temperature: - 55°C to + 85°C. (To + 125°C with voltage derating.)

Capacitance Range: 15µF to 680µF

Capacitance Tolerance: ±10%, ±20% standard.

Voltage Rating: 4WVDC to 10WVDC, (16WVDC, 20WVDC, and 25WVDC under development)

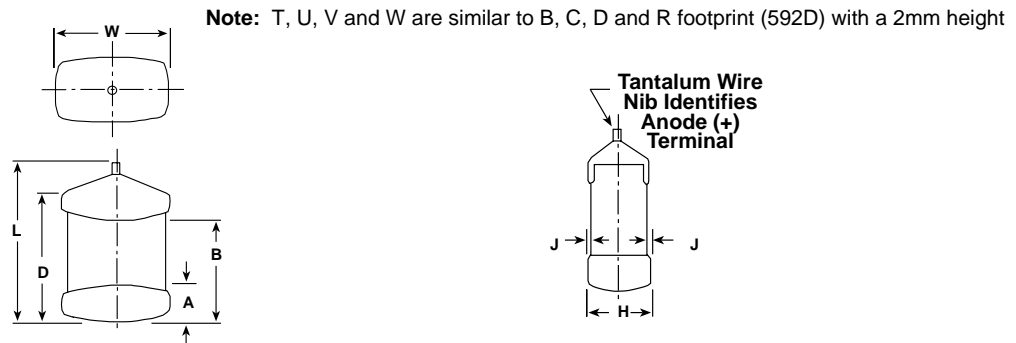
ORDERING INFORMATION

592D TYPE	477 CAPACITANCE	X0 CAPACITANCE TOLERANCE	6R3 DC VOLTAGE RATING @ + 85°C	W CASE CODE	2 TERMINATION	T REEL SIZE AND PACKAGING
	This is expressed in picofarads. The first two digits are the significant figures. The third is the number of zeros to follow.	X0 = ± 20% X9 = ± 10%	This is expressed in volts. To complete the three-digit block, zeros precede the voltage rating. A decimal point is indicated by an "R" (6R3 = 6.3 volts).	See Ratings and Case Codes Table.	2 = Solderable Coating. 4 = Gold Plated	T = Tape and Reel 7" [178mm] Reel W = 13" [330mm] Reel

Note: Preferred Tolerance and reel sizes are in bold.

We reserve the right to supply higher voltage ratings and tighter capacitance tolerance capacitors in the same case size. Voltage substitutions will be marked with the higher voltage rating

DIMENSIONS in inches [millimeters]



CASE CODE	L (MAX.)	W	H max	A	B	D (REF.)	J (MAX.)
T	0.158 [4.0]	0.116 ±0.010 [2.8 ±0.3]	0.079 [2.0]	0.031 ±0.012 [0.8 ±0.3]	0.097 ±0.015 [2.5 ±0.4]	0.138 [3.5]	0.004 [0.1]
U	0.281 [7.1]	0.126 ±0.010 [3.2 ±0.3]	0.079 [2.0]	0.051 ±0.012 [1.3 ±0.3]	0.180 ±0.025 [4.6 ±0.6]	0.236 [6.0]	0.004 [0.1]
V	0.293 [7.5]	0.170 ±0.010 [4.3 ±0.3]	0.079 [2.0]	0.051 ±0.012 [1.3 ±0.3]	0.180 ±0.025 [4.6 ±0.6]	0.253 [6.4]	0.004 [0.1]
W	0.285 [7.2]	0.235 ±0.010 [6.0 ±0.3]	0.079 [2.0]	0.051 ±0.012 [1.3 ±0.3]	0.180 ±0.025 [4.6 ±0.6]	0.243 [6.2]	0.004 [0.1]

Note: The anode termination (D less B) will be a minimum of .010" [0.3mm].



RATINGS AND CASE CODES						
μF	4 V	6.3 V	10 V	*16 V	*20 V	*25 V
15						U
22				T	U	V
33			T	T	V	
47			T	U/T		
68		T	U/T	V/U		
100	T	T	U	V		
150	T	U	V/U	W		
220	U	V/U	W/V			
330	V/U	W/V	W			
470	W/V	W				
680	W					

Bold characters identify extended range ratings

* Voltage range under development

STANDARD / EXTENDED RATINGS						
CAPACITANCE (μF)	CASE CODE	PART NUMBER	Max. DCL @ + 25°C (μA)	Max. DF @ + 25°C 120 Hz (%)	Max. ESR @ + 25°C 100kHz (Ohms)	Max. RIPPLE 100kHz I _{rms} (Amps)
4 WVDC @ + 85°C, SURGE = 5.2 V . . . 2.7 WVDC @ + 125°C, SURGE = 3.4 V						
100	T	592D107X_004T2T	4	8	0.45	0.42
150	T	592D157X_004T2T	6	8	0.45	0.42
220	U	592D227X_004U2T	8.8	8	0.19	0.76
330	V	592D337X_004V2T	13.2	8	0.12	1.08
330	U	592D337X_004U2T	13.2	8	0.15	0.86
470	W	592D477X_004W2T	18.8	8	0.10	1.32
470	V	592D477X_004V2T	18.8	10	0.10	1.18
680	W	592D687X_004W2T	27.2	12	0.10	1.32
6.3 WVDC @ + 85°C, SURGE = 8 V . . . 4 WVDC @ + 125°C, SURGE = 5 V						
68	T	592D686X_6R3T2T	4.3	6	0.50	0.40
100	T	592D107X_6R3T2T	6.3	8	0.45	0.42
150	U	592D157X_6R3U2T	9.5	8	0.19	0.76
220	V	592D227X_6R3V2T	13.9	8	0.12	1.08
220	U	592D227X_6R3U2T	13.9	8	0.15	0.86
330	W	592D337X_6R3W2T	20.8	8	0.10	1.32
330	V	592D337X_6R3V2T	20.8	8	0.10	1.18
470	W	592D477X_6R3W2T	29.6	10	0.10	1.32



STANDARD / EXTENDED RATINGS						
CAPACITANCE (μ F)	CASE CODE	PART NUMBER	Max. DCL @ + 25°C (μ A)	Max. DF @ + 25°C 120 Hz (%)	Max. ESR @ + 25°C 100kHz (Ohms)	Max. RIPPLE 100kHz Irms (Amps)
10 WVDC @ + 85°C, SURGE = 13 V . . . 7 WVDC @ + 125°C, SURGE = 8 V						
33	T	592D336X_010T2T	3.3	6	0.50	0.40
47	T	592D476X_010T2T	4.7	6	0.50	0.40
68	U	592D686X_010U2T	6.8	6	0.25	0.66
68	T	592D686X_010T2T	6.8	6	0.45	0.42
100	U	592D107X_010U2T	10	8	0.19	0.76
150	V	592D157X_010V2T	15	8	0.14	1.00
150	U	592D157X_010U2T	15	8	0.17	0.80
220	W	592D227X_010W2T	22	8	0.10	1.32
220	V	592D227X_010V2T	22	8	0.12	1.08
330	W	592D337X_010W2T	33	8	0.10	1.32
*16 WVDC @ + 85°C, SURGE = 20 V . . . 10 WVDC @ + 125°C, SURGE = 12 V						
22	T	592D226X_016T2T	3.5	6	0.60	0.36
33	T	592D336X_016T2T	5.3	6	0.60	0.36
47	U	592D476X_016U2T	7.5	6	0.25	0.66
47	T	592D476X_016T2T	7.5	6	0.45	0.42
68	V	592D686X_016V2T	10.9	6	0.17	0.91
68	U	592D686X_016U2T	10.9	6	0.25	0.66
100	V	592D107X_016V2T	16	8	0.15	0.97
150	W	592D157X_016W2T	24	8	0.10	1.32
*20 WVDC @ + 85°C, SURGE = 23 V . . . 13 WVDC @ + 125°C, SURGE = 17 V						
22	U	592D226X_020U2T	4.4	6	0.30	0.61
33	V	592D336X_020V2T	6.6	6	0.26	0.73
*25 WVDC @ + 85°C, SURGE = 33 V . . . 16 WVDC @ + 125°C, SURGE = 20 V						
15	U	592D156X_025U2T	3.8	6	0.40	0.52
22	V	592D226X_025V2T	5.5	6	0.30	0.68

Note: Voltage range under development



PERFORMANCE CHARACTERISTICS

1. **Operating Temperature:** Capacitors are designed to operate over the temperature range - 55°C to + 85°C.

1.1 Capacitors may be operated to + 125°C with voltage derating to two-thirds the + 85°C rating.

+ 85°C Rating		+ 125°C Rating	
Working Voltage (V)	Surge Voltage (V)	Working Voltage (V)	Surge Voltage (V)
4	5.2	2.7	3.4
6.3	8	4	5
10	13	7	8
16	20	10	12
20	26	13	16
25	32	17	20

2. **DC Working Voltage:** The DC working voltage is the maximum operating voltage for continuous duty at the rated temperature.

3. **Surge Voltage:** The surge DC rating is the maximum voltage to which the capacitors may be subjected under any conditions, including transients and peak ripple at the highest line voltage.

3.1 **Surge Voltage Test:** Capacitors shall withstand the surge voltage applied in series with a 33 ohm $\pm 5\%$ resistor at the rate of one-half minute on, one-half minute off, at + 85°C, for 1000 successive test cycles.

3.2 Following the surge voltage test, the dissipation factor and the leakage current shall meet the initial requirements; the capacitance shall not have changed more than $\pm 10\%$.

4. **Capacitance Tolerance:** The capacitance of all capacitors shall be within the specified tolerance limits of the normal rating.

4.1 Capacitance measurements shall be made by means of polarized capacitance bridge. The polarizing voltage shall be of such magnitude that there shall be no reversal of polarity due to the AC component. The maximum voltage applied to capacitors during measurement shall be 2 volts rms at 120 Hz at +25°C. If the AC voltage applied is less than one-half volt rms, no DC bias is required. Accuracy of the bridge shall be within $\pm 2\%$.

5. **Capacitance Change With Temperature:** The capacitance change with temperature shall not exceed the following percentage of the capacitance measured at + 25°C:

- 55°C	+ 85°C	+ 125°C
- 10%	+ 10%	+ 12%

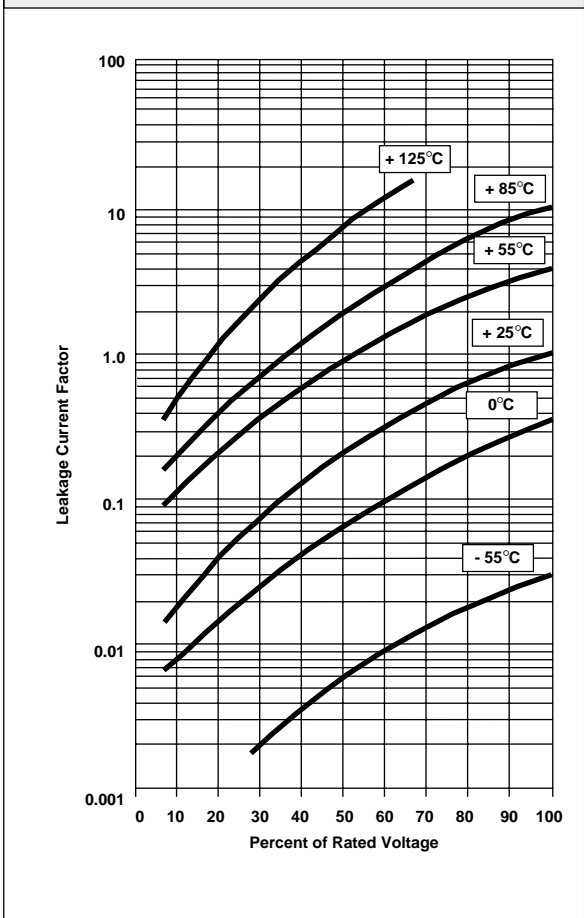
6. **Dissipation Factor:** The dissipation factor, determined from the expression $2\pi fRC$, shall not exceed values listed in the Standard Ratings Table.

6.1 Measurements shall be made by the bridge method at, or referred to, a frequency of 120 Hz and a temperature of + 25°C.

7. **Leakage Current:** Capacitors shall be stabilized at the rated temperature for 30 minutes. Rated voltage shall be applied to capacitors for 5 minutes using a steady source of power (such as a regulated power supply) with 1000 ohm resistor connected in series with the capacitor under test to limit the charging current. Leakage current shall then be measured.

Note that the leakage current varies with temperature and applied voltage. See graph below for the appropriate adjustment factor.

TYPICAL LEAKAGE CURRENT FACTOR RANGE





PERFORMANCE CHARACTERISTICS (Continued)

- 7.1 At + 25°C, the leakage current shall not exceed the value listed in the Standard Ratings Table.
- 7.2 At + 85°C, the leakage current shall not exceed 10 times the value listed in the Standard Ratings Table.
- 7.3 At + 125°C, the leakage current shall not exceed 12 times the value listed in the Standard Ratings Table.
- 8. **Equivalent Series Resistance:** Measurements shall be made by the bridge method at, or referred to, a frequency of 100 KHz and a temperature of + 25°C.
- 8.1 The Equivalent Series Resistance shall not exceed the value listed in the Standard Ratings Table.
- 9. **Life Test:** Capacitors shall withstand rated DC voltage applied at + 85°C for 2000 hours or derated DC voltage applied at + 125°C for 1000 hours.
- 9.1 Following the life test, the dissipation factor and leakage shall meet the initial requirement; the capacitance change shall not exceed ± 10% of the initial value.
- 10 **Humidity Test:** Capacitors shall withstand 1000 hours at + 40°C, 90% to 95% relative humidity, with no voltage applied
- 10.1 Following the humidity test, capacitance change shall not exceed ± 10% of the initial value, dissipation factor shall not exceed 150% of the initial requirement; leakage current shall not exceed 200% of the initial requirement at + 25°C
- 11. **Solderability:** Capacitors will meet the solderability requirements of ANSI/J-STD-002, test B category 1.
- 12. **Resistance to Soldering Heat:** Capacitors mounted on a substrate will withstand + 260°C for 5 seconds.
- 12.1 Following the resistance to soldering heat test, capacitance, dissipation factor and DC leakage current shall meet the initial requirement.
- 13. **Marking:** The small body area of these capacitors does not allow elaborate marking schemes. All required information is present on the carton or package in which the parts are shipped; in addition, part number, quantity and data code are indicated on the reels.
- 14. **Terminal Strength:** Per IEC-384-3, minimum of 5N shear force.
- 15. **Environmental:** Mercury, CFC and ODS materials are not used in the manufacture of these capacitors.
- 16. **Flammability:** Encapsulant materials meet UL94 V0
- 17. **Capacitor Failure Mode:** The predominant failure mode for solid tantalum capacitors is increased leakage current resulting in a shorted circuit. Capacitor failure may result from excess forward or reverse DC voltage, surge current, ripple current, thermal shock or excessive temperature.

The increase in leakage is caused by a breakdown of the Ta₂O₅ dielectric. For additional information on leakage failure of solid tantalum chip capacitors, refer to Vishay Sprague Technical Paper, "Leakage Failure Mode in Solid Tantalum Chip Capacitors."

GUIDE TO APPLICATION

1.0 Recommended rated working voltage guidelines: (-55°C to + 85°C)

Application Voltage (V)	Recommended Capacitor Voltage Rating (V)
2.5	4
4	6.3
5	8
6	10
10	16
12	20
18	25

2. **A-C Ripple Current:** The maximum allowable ripple current shall be determined from the formula:

$$I_{rms} = \sqrt{\frac{P}{R_{ESR}}}$$

where,

P = Power Dissipation in Watts @ + 25°C as given in the table in Paragraph Number 6.0 (Power Dissipation)

R_{ESR} = The capacitor Equivalent Series Resistance at the specified frequency.

3. **A-C Ripple Voltage:** The maximum allowable ripple voltage shall be determined from the formula:

$$V_{rms} = Z \sqrt{\frac{P}{R_{ESR}}}$$

or, from the formula:

$$V_{rms} = I_{rms} \times Z$$

where,

P = Power Dissipation in Watts @ + 25°C as given in the table in Paragraph Number 6.0 (Power Dissipation).

R_{ESR} = The capacitor Equivalent Series Resistance at the specified frequency.

GUIDE TO APPLICATION (Continued)

Z = The capacitor impedance at the specified frequency.

- 3.1 The sum of the peak AC voltage plus the applied DC voltage shall not exceed the DC voltage rating of the capacitor.
- 3.2 The sum of the negative peak AC voltage plus the applied DC voltage shall not allow a voltage reversal exceeding 10% of the DC working voltage at + 25°C.
- 4.0 **Reverse Voltage:** These capacitors are capable of withstanding peak voltages in the reverse direction equal to 10% of the DC rating or 1 volt maximum at + 25°C and 5% of the DC voltage rating or 0.5 volt maximum at + 85°C.
- 5.0 **Temperature Derating:** If these capacitors are to be operated at temperatures above + 25°C, the permissible rms ripple current or voltage shall be calculated using the derating factors as shown:

Temperature	Derating Factor
+ 25°C	1.0
+ 85°C	0.9
+ 125°C	0.4

- 6.0 **Power Dissipation:** Power dissipation will be affected by the heat sinking capability of the mounting surface. Non-sinusoidal ripple current may produce heating effects which differ from those shown. It is important that the equivalent *I_{rms}* value be established when calculating permissible operating levels. (Power dissipation calculated using + 25°C temperature rise.)

Case Code	Maximum Permissible Power Dissipation @ +25°C (Watts) in free air
T	0.080
U	0.110
V	0.140
W	0.175

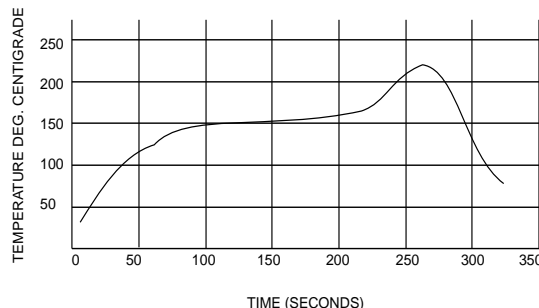
- 7.0 **Printed Circuit Board Materials:** The capacitors are compatible with most commonly used printed circuit board materials (alumina substrates, FR4, FR5, G10, PTFE-fluorocarbon and porcelainized steel). If your desired board material is not shown there please contact the Tantalum Marketing Department for assistance in determining compatibility.

8. Attachment:

- 8.1 **Solder Paste:** The recommended thickness of the solder paste after application is 0.007" ± .001" [.178mm ± .025mm]. Care should be exercised in selecting the solder paste. The metal purity should be as high as practical. The flux (in the paste) must be active enough to remove the oxides formed on the metallization prior to the exposure to soldering heat.
- 8.2 **Soldering:** Capacitors can be attached by conventional soldering techniques - convection, infrared reflow, wave soldering and hot plate methods.

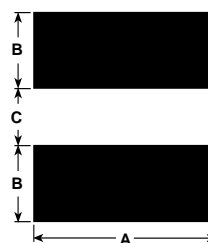
The Soldering Profile chart shows typical recommended time/temperature conditions for soldering. Attachment with a soldering iron is not recommended due to the difficulty of controlling temperature and time at temperature. The soldering iron must never come in contact with the capacitor.

RECOMMENDED REFLOW SOLDERING PROFILE



- 9.0 **Recommended Mounting Pad Geometries:** The nib must have sufficient clearance to avoid electrical contact with other components. The width dimension indicated is the same as the maximum width of the capacitor. This is to minimize lateral movement.

REFLOW SOLDER PADS* in inches [millimeters]



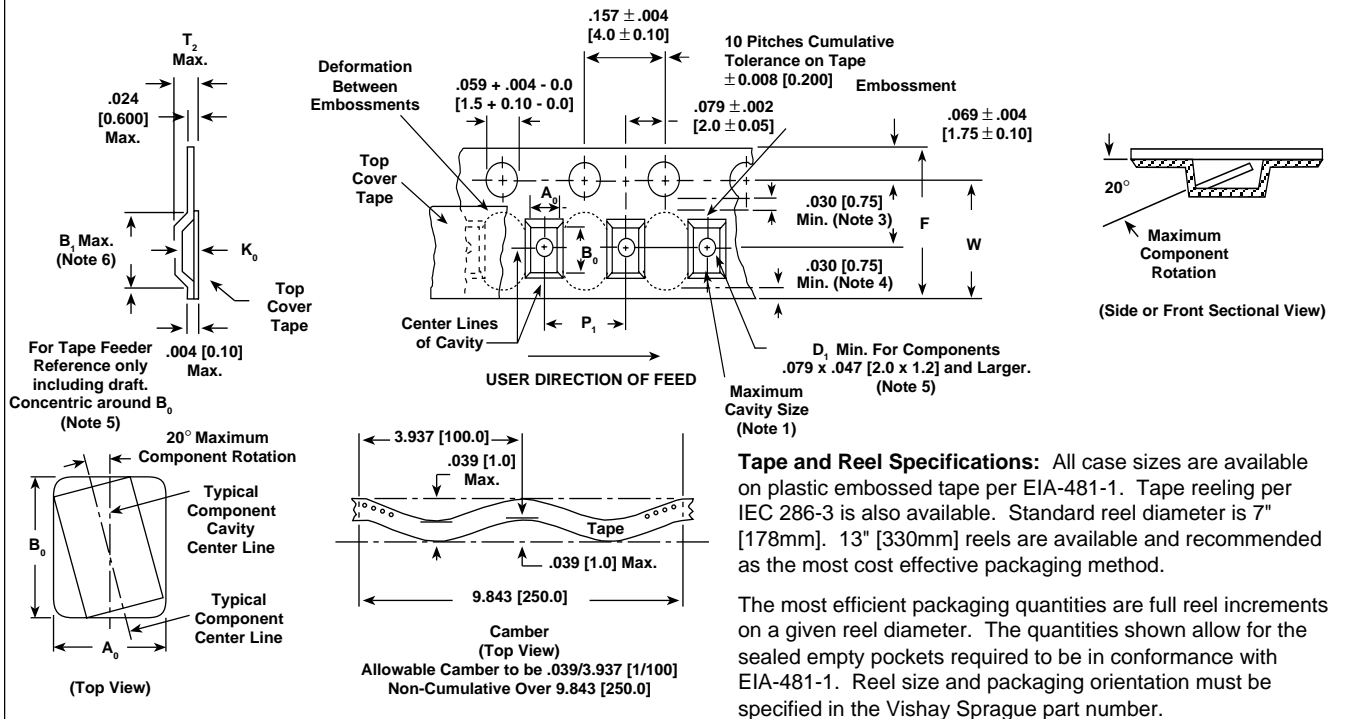
CASE CODE	WIDTH (A)	PAD METALIZATION	SEPARATION (C)
T	.120 [3.5]	.065 [1.7]	.065 [1.7]
U	.136 [3.5]	.090 [2.3]	.120 [3.1]
V	.180 [4.6]	.090 [2.3]	.145 [3.7]
W	.245 [6.3]	.090 [2.3]	.145 [3.7]

* Pads for T, U and V case codes are otherwise pad compatible with Type 293D, B, C and D case codes respectively.

- 10.0 **Cleaning (Flux Removal) After Soldering:** The capacitors are compatible with all commonly used solvents such as TES, TMS, Prelete, Chloroethane, Terpene and aqueous cleaning media. Solvents containing methylene chloride or other epoxy solvents should be avoided since these will attack the epoxy encapsulation material.

TAPE AND REEL PACKAGING in inches [millimeters]

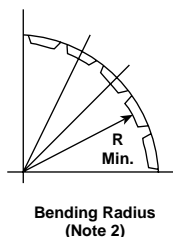
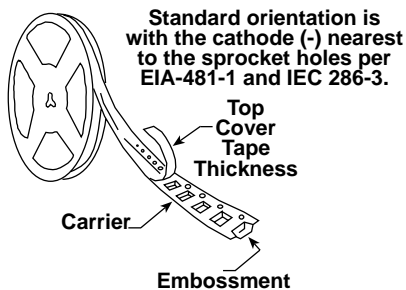
Note: Metric dimensions will govern. Dimensions in inches are rounded and for reference only.



TAPE SIZE	B_1 (Max.) (Note 6)	D_1 (Min.) (Note 5)	F	P_1	R (Min.) (Note 2)	T_2 (Max.)	W	$A_0 B_0 K_0$
8mm	.179 [4.55]	.039 [1.0]	.138 ± .002 [3.5 ± 0.05]	.157 ± .004 [4.0 ± 0.10]	.984 [25.0]	.098 [2.5]	.315 ± .012 - .004 [8.0 + 0.3 - 0.1]	(Note 1)
12mm	.323 [8.2]	.059 [1.5]	.217 ± .002 [5.5 ± 0.05]	.157 ± .004 [4.0 ± 0.10]	1.181 [30.0]	.256 [6.5]	.472 ± .012 [12.0 ± .30]	

Notes:

- $A_0 B_0 K_0$ are determined by the maximum dimensions to the ends of the terminals extending from the component body and/or the body dimensions of the component. The clearance between the ends of the terminals or body of the component to the sides and depth of the cavity ($A_0 B_0 K_0$) must be within .002" [0.05mm] minimum and .020" [0.50mm] maximum. The clearance allowed must also prevent rotation of the component within the cavity of not more than 20 degrees.
- Tape with components shall pass around radius "R" without damage. The minimum trailer length may require additional length to provide R minimum for 12mm embossed tape for reels with hub diameters approaching N minimum.
- This dimension is the flat area from the edge of the sprocket hole to either the outward deformation of the carrier tape between the embossed cavities or to the edge of the cavity whichever is less.
- This dimension is the flat area from the edge of the carrier tape opposite the sprocket holes to either the outward deformation of the carrier tape between the embossed cavity or to the edge of the cavity whichever is less.
- The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.
- B_1 dimension is a reference dimension for tape feeder clearance only.



CASE CODE	TAPE WIDTH	COMPONENT PITCH	Units Per Reel	
			7" [178] REEL	13" [303] REEL
T	12mm	4mm	2000	8000
U	12mm	8mm	1000	4000
V	12mm	8mm	1000	4000
W	12mm	8mm	1000	2500